DIGITAL SIGNAL PROCESSOR WITH MULTIPLE INSTRUCTION DESTINATIONS FOR A PIPELINED SYSTEM
Gregory A. Overkamp et al.
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10559-270001



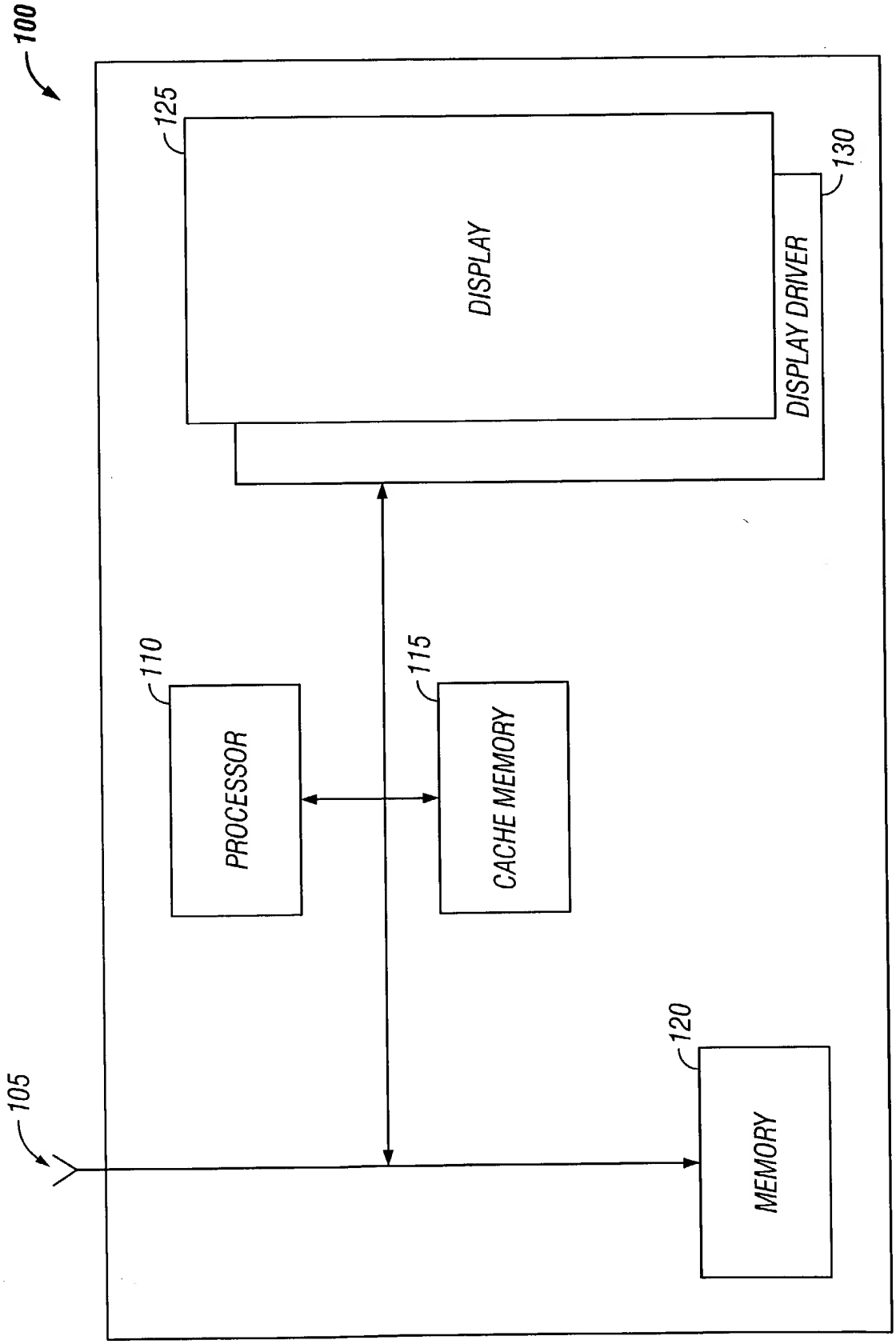


FIG. 1

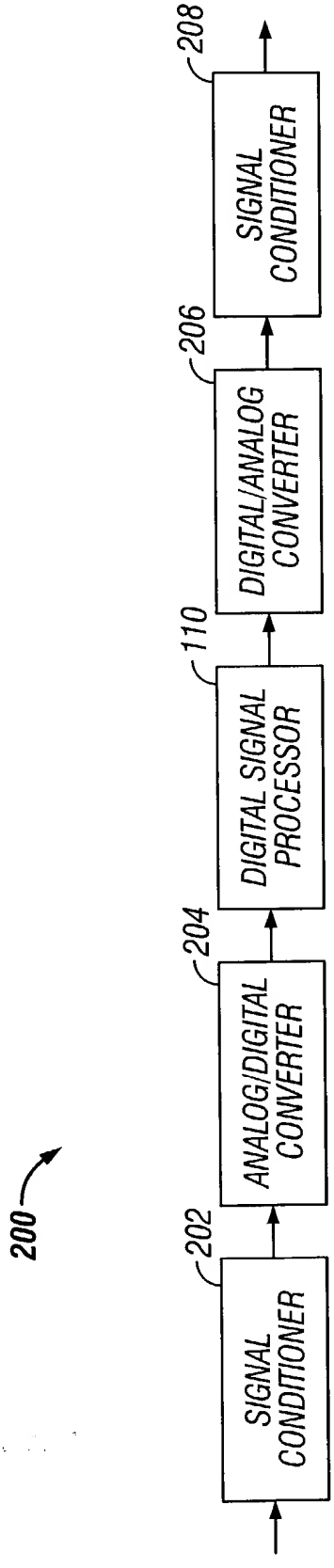


FIG. 2

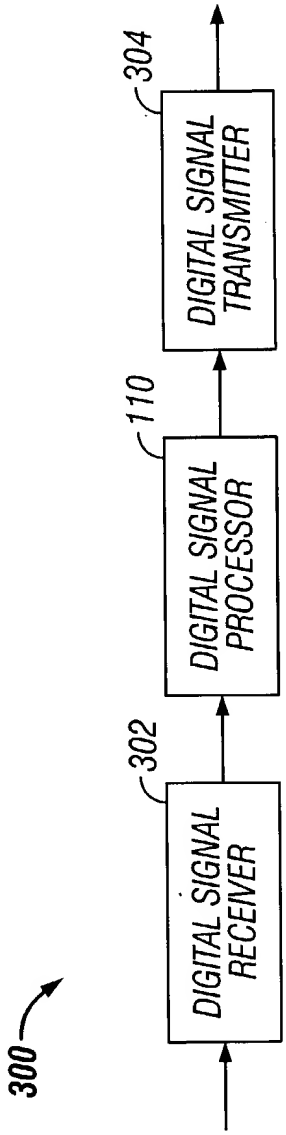


FIG. 3

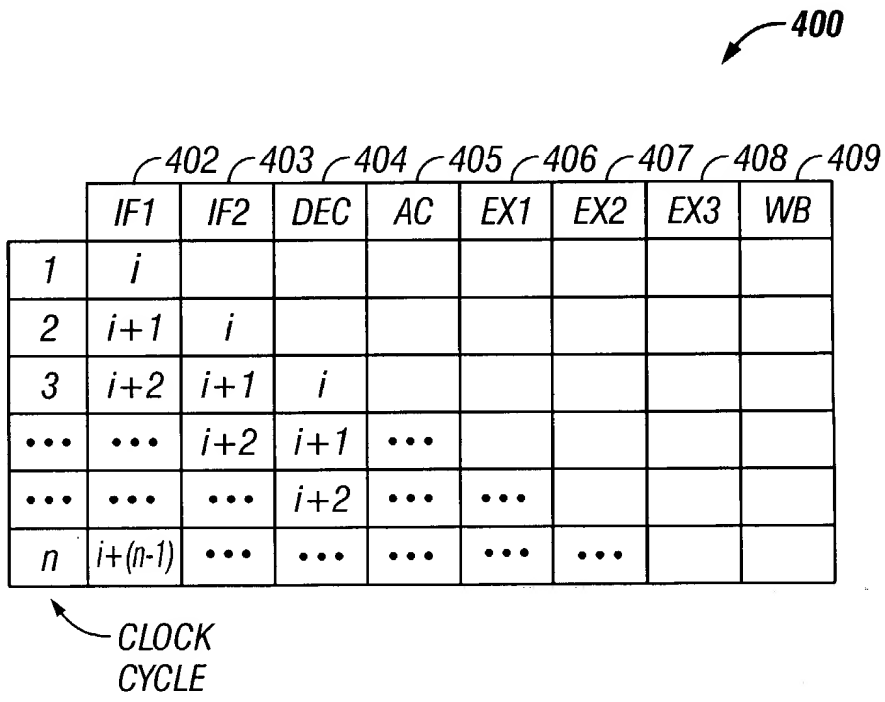


FIG. 4

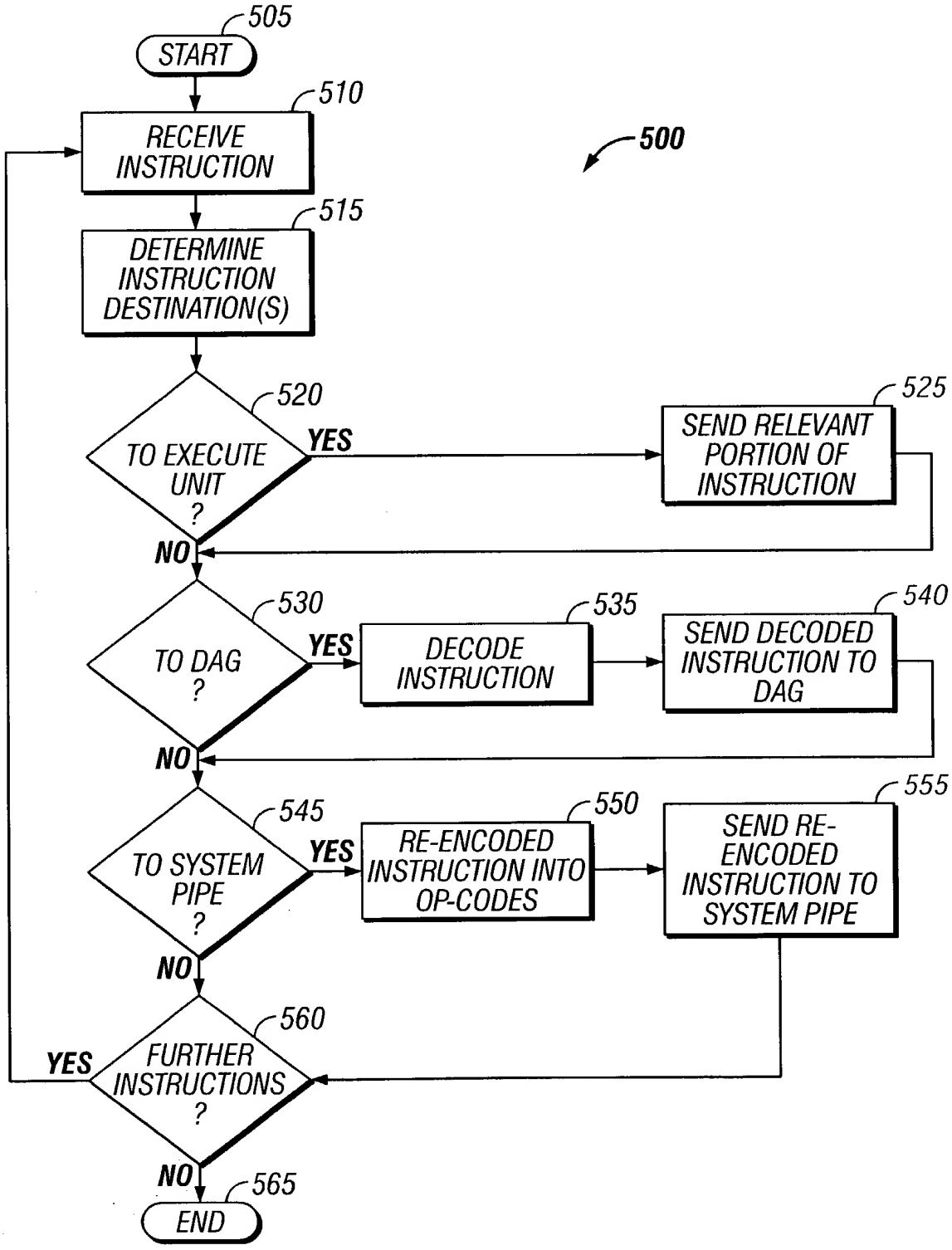


FIG. 5